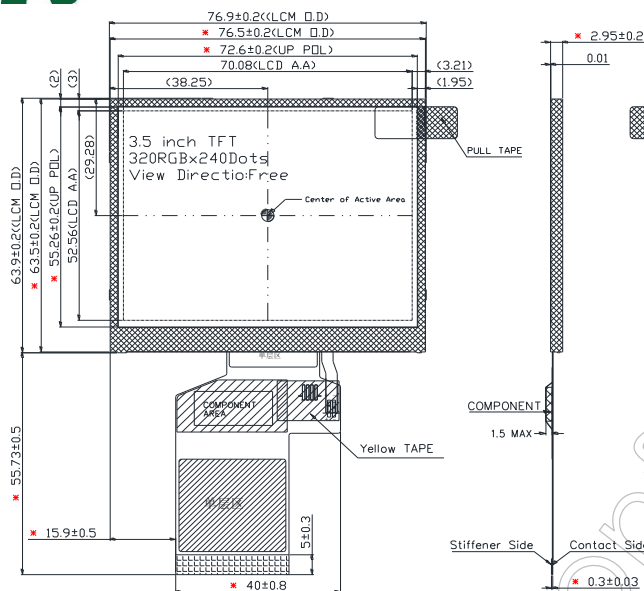
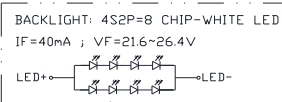


LED CIRCUIT DIAGRAM:



Parameter	Specification	Unit	Pin No.	Symbol	I/O	Description
LCD size	3.5 (Diagonal)	inch	1-2	VLEDK	P	Power for LED backlight (Cathode) .
Number Of Pixels	320(H)×240(V)	pixels	3-4	VLEDA	P	Power for LED backlight (Anode) .
Pixel Pitch	0.219(H)×RGB×0.219(V)	mm	5	NC(YU)	-	NC(Touch panel Top Film Terminal).
Active Area	70.08(H)×52.56(V)	mm	6	NC(XR)	-	NC(Touch panel Right Glass Terminal).
Module Size	76.5(W)×63.7(H)×2.95(D)	mm	7	NC	-	No connection.
Display Mode	Normally Black, Transmissive		8	RESET	I	Global reset. Active low, Internal pull high.
Display Colors	16.7M	colors	9	SPENA	I	SPI interface data enable signal.
Interface	SPI+RGB 24bit		10	SPCLK	I	SPI interface clock.
View Direction	ALL	O'clock	11	SPDAT	I	SPI interface data.
Power Consumption	1.0(Typ.)	W	12-19	B0-B7	I	Blue data input.
Weight	26(Typ.)	g	20-27	G0-G7	I	Green data input.
Luminance	1000(Typ.)	cd/m ²	28-35	R0-R7	I	Red data input.
Driver IC	ST7272A		36	HSYNC	I	Horizontal sync signal.
Operating Temperature	-20~+70	°C	37	VSYSN	I	Vertical sync signal.
Storage Temperature	-30~+80	°C	38	DCLK	I	Pixel clock input pin.
			39-40	NC	-	No connection.
			41-42	VCC	P	System power
			43	NC(YD)	-	NC(Touch panel Bottom Film Terminal).
			44	NC(XL)	-	NC(Touch panel LEFT Glass Terminal).
			45-48	NC	-	No connection.
			49	PS	I	Set parallel or serial RGB interface L Serial 8 bit RGB interface H Parallel 24 bit RGB interface
			50-51	NC	-	No connection.
			52	DE	I	Data enable pin
			53-54	AVSS	P	Ground for digital circuits.
Parameter	Symbol	Min.	Typ.	Max.	Unit	
Power Supply Voltage	VDD	3.0	3.3	3.6	V	
IO Supply Voltage	VDDI	3.0	3.3	3.6	V	
Input logic high voltage	VIH	0.7VDDI	-	VDDI	V	
Input logic low voltage	VIL	0	-	0.3VDDI	V	
Output logic high voltage	VOH	VDDI-0.4	-	VDDI	V	
Output logic low voltage	VOL	0	-	0.4	V	
Parameter	Symbol	Min.	Typ.	Max.	Unit	
LED Power supply Input voltage	VF	21.6	24.0	26.4	V	
Power supply current for Back light	IF	--	40	--	mA	
LED Lifetime	--	--	30000	--	Hrs	