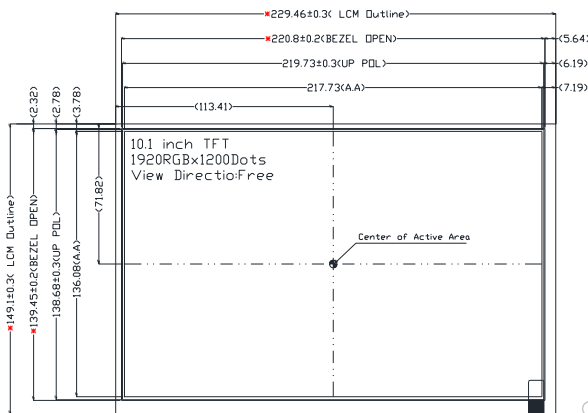


Parameter	Specification	Unit
LCD size	10.1 (Diagonal)	inch
Number Of Pixels	1920(H)×1200(V)	pixels
Pixel Pitch	0.1134(H)×0.1134(V)	mm
Active Area	217.728(H)×136.08(V)	mm
Module Size	229.46(W)×149.1(H)×4.8(D)	mm
Display Mode	Normally Black, Transmissive	
Interface	LVDS	
View Direction	ALL	O'clock
Display Colors	16.7M	Colors
Power Supply	3.3	V
Power Consumption	TBD	W
Weight	TBD	g
Luminance	1000 (Typ.)	cd/m ²
Driver IC	/	
Operating Temperature	-20~+80	°C
Storage Temperature	-30~+85	°C

Parameter	Symbol	Min.	Typ.	Max.	Unit
Power Supply Voltage	VDD	2.3	3.3	3.6	V
	VRP	--	--	300	mV
Power Supply current	IDD	--	300	360	mA
Power Consumption	PLCD	--	1.0	1.2	W
Rush current	IRUSH	--	--	3.0	A
Input logic high voltage	VIH	2.7	--	3.3	V
Input logic low voltage	VIL	0	--	0.5	V
Output logic high voltage	VOH	2.7	--	3.3	V
Output logic low voltage	VOL	0	--	0.5	V

Parameter	Symbol	Min.	Typ.	Max.	Unit
Forward voltage	VF	27.0	30.5	34.0	V
Forward current	IF	---	240	--	mA
LED Lifetime	--	30000	--	--	Hrs

Pin.	Symbol	I/O	Description
1-2	LEDK	P	Power supply for LED cathode input.
3-4	LEDA	P	Power supply for LED anode input.
5	NC	-	No connection.
6	GND	P	Ground.
7	ERXIN3P	I	LVDS Positive differential data(Even data).
8	ERXIN3N	I	LVDS Negative differential data(Even data).
9	GND	P	Ground.
10	ERXIN2P	I	LVDS Positive differential data(Even data).
11	ERXIN2N	I	LVDS Negative differential data(Even data).
12	GND	P	Ground.
13	ERXCLKP	I	LVDS Positive differential clock signal(Even clock).
14	ERXCLKN	I	LVDS Negative differential clock signal(Even clock).
15	GND	P	Ground.
16	ERXIN1P	I	LVDS Positive differential data(Even data).
17	ERXIN1N	I	LVDS Negative differential data(Even data).
18	GND	P	Ground.
19	ERXIN0P	I	LVDS Positive differential data(Even data).
20	ERXIN0N	I	LVDS Negative differential data(Even data).
21	GND	P	Ground.
22	ORXIN3P	I	LVDS Positive differential data(Odd data).
23	ORXIN3N	I	LVDS Negative differential data(Odd data).
24	GND	P	Ground.
25	ORXIN2P	I	LVDS Positive differential data(Odd data).
26	ORXIN2N	I	LVDS Negative differential data(Odd data).
27	GND	P	Ground.
28	ORXCLKP	I	LVDS Positive differential clock signal(Odd clock).
29	ORXCLKN	I	LVDS Negative differential clock signal(Odd clock).
30	GND	P	Ground.
31	ORXIN1P	I	LVDS Positive differential data(Odd data).
32	ORXIN1N	I	LVDS Negative differential data(Odd data).
33	GND	P	Ground.
34	ORXIN0P	I	LVDS Positive differential data(Odd data).
35	ORXIN0N	I	LVDS Negative differential data(Odd data).
36	GND	P	Ground.
37	IIC_SDA	I	OTP_SDA.
38	IIC_SCL	I	OTP_SCL.
39	VDD_OTP	P	OTP Power supply VDD_OTP=8.6V.
40	EEPEN	-	No connection.
41-45	VDDIN	P	Power supply VDDIN=3.3V(Typ.)