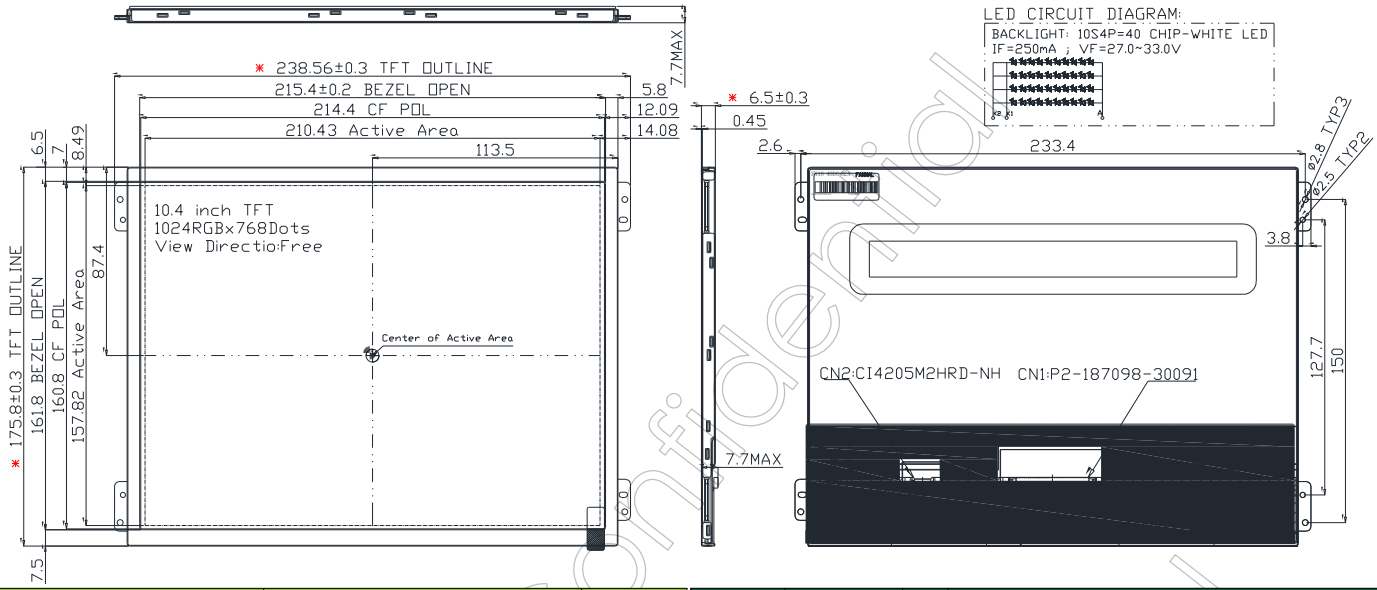
FN1040D002A



Parameter	Specification	Unit
LCD size	10.4 (Diagonal)	inch
Resolution	1024(H)×768(V)	pixels
Sub pixel size	0.2055(H)×0.2055(V)	mm
Active Area	210.432(H)×157.824(V)	mm
Display Mode	Normally Black, Transmissive	
View direction	ALL	O'clock
Module Size	238.6(W)×175.8(H)×6.8(D)	mm
Pixel driving element	a-Si TFT	
Interface	LVDS	
Driver IC	/	
Weight	305(Typ.)	g
Luminance	700(Typ.)	cd/m²
Operating Temperature	-30~+80	℃
Storage Temperature	-30~+80	℃

Parameter	Symbol	Min.	Typ.	Max.	Unit
Power Voltage	VDD	3.0	3.3	3.6	V
	LED_VCCS	11	12	13	V
Input logic high voltage	VIH	0.7VDD	-	VDD	V
Input logic low vogate	VIL	0	-	0.3VDD	V
Current for Power	IVDD		385	424	mA
	ILED_VCCS	-	0.52	-	A
LED_EN Control Level	BL On	3.0	-	5.0	V
	BL Off	0	-	0.3	V
LED_PWM Control Level	PWM High Level	3.0	-	5.0	V
	PWM Low Level	0	-	0.3	V
LED_PWM Control Frequency	fpwm	1K	-	20K	Hz

Pin No.	Symbol	I/O	Description
1	NC	-	Reserved as BIST function/No connection
2	GND	P	Ground
3	Rin3+	I	Positive LVDS differential data input
4	Rin3-	I	Negative LVDS differential data input
5	GND	P	Ground
6	CLK+	I	Positive Clock signal
7	CLK-	I	Negative Clock signal
8	GND	P	Ground
9	Rin2+	I	Positive LVDS differential data input
10	Rin2-	I	Negative LVDS differential data input
11	GND	P	Ground
12	Rin1+	I	Positive LVDS differential data input
13	Rin1-	I	Negative LVDS differential data input
14	GND	P	Ground
15	Rin0+	I	Positive LVDS differential data input
16	Rin0-	I	Negative LVDS differential data input
17	GND	P	Ground
18	NC	-	No connection
19	GND	P	Ground
20	SEL6/8	I	Selection for 6 bits/8bits LVDS data input Low or NC：8bit input mode High：6bit input mode
21-23	NC	-	No connection
24	Reverse	I	Reverse panel function(Display rotation)
25-27	GND	P	Ground
28-30	VDD	P	Power supply:+3.3V

Parameter	Symbol	Min.	Typ.	Max.	Unit
Voltage for LED backlight	LED+	31	33	35	V
Current for LED backlight	LED1- LED2-	78	80	82	mA